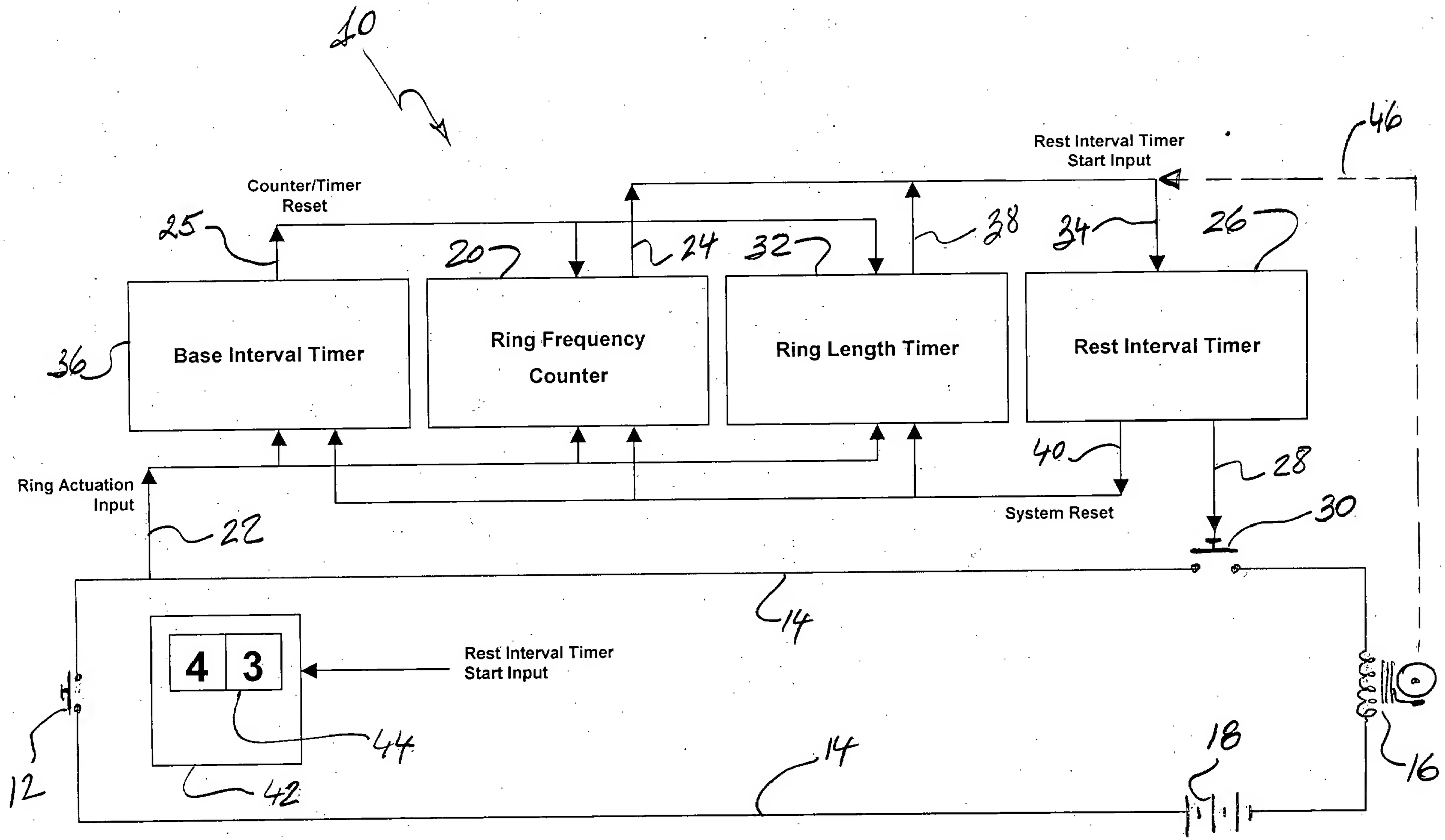
Fig. 1


10
46
Rest Interval Timer
Start Input
Counter/Timer
Reset
25
20
24
32
38
34
26
36
Base Interval Timer
Ring Frequency
Counter
Ring Length Timer
Rest Interval Timer
40
28
Ring Actuation
Input
22
System Reset
30
14
4
3
Rest Interval Timer
Start Input
44
12
42
14
18
16